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L6	5	((("6262711") or ("5768134") or ("5813984") or ("5953686") or ("3843132"))).PN.	US-PGPUB; USPAT	OR	OFF	2006/01/06 10:27
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L9	18	("3484984"   "3696548"   "4348191"   "4552541"   "4556393"   "4743202"   "5013047"   "5154615"   "5411428"   "5742486"   "5779515"   "5788553"   "5853327"   "5906369"   "6190174"   "6206745"   "6227931").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/06 10:27
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L15	629	virtual adj model	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L16	667	(baseboard breadboard) and sensor and component	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L17	6	L15 and L16	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L18	310	L16 and model	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
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5.	(pub-date > 1959 and pub-date < 2002 and TITLE-ABSTR-KEY((baseboard or breadboard))) and model [All Sources(- All Sciences -)]	44
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3.	((pub-date > 1959 and pub-date < 2002 and FULL-TEXT(virtual model) and FULL-TEXT(component)) and display) and sensor [All Sources(- All Sciences -)]	20
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<u>#4</u>	((((model<and>sensor)<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)) <AND> (((mount*<and>structure)<and>(baseboard<or>breadboard)) <and> (pyr >= 1951 <and> pyr <= 2001)))	52
<u>#5</u>	(((((model<and>sensor)<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)) <AND> (((mount*<and>structure)<and>(baseboard<or>breadboard)) <and> (pyr >= 1951 <and> pyr <= 2001))) <AND> (((virtual model<and>display)) <and> (pyr >= 1951 <and> pyr <= 2001)))	0
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a bus or a mesh (see Fig. 1)Whereas this physical **model** dominates, disagreement exists as to the  
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a bus or a mesh (see Fig. 1)Whereas this physical **model** dominates, disagreement exists as to the  
approaches have been taken to verify a design: **breadboard** prototyping and software simulation. A  
them was valid only for the particular hardware. **Breadboard** prototypes are also extensively used in  
[usc.edu/pub/CENG/1994/CENG-94-15.ps.Z](http://usc.edu/pub/CENG/1994/CENG-94-15.ps.Z)

[Yearly operation profile of the ATLAS SCT detectors - January Kondo \(Correct\)](#)  
i T 2 E g 2k B T -exp 3 -1.5 Module **model** Two designs of the ATLAS barrel module are  
difference of the design is the shape of the TPG **baseboard** as shown in Figure 3 and Figure 4. 1.6 Thermal  
[atlas.kek.jp/~kondo/sct/INDET-NO-203.ps](http://atlas.kek.jp/~kondo/sct/INDET-NO-203.ps)

[Modeling Analog Circuitry With VHDL - Mueller \(1994\) \(Correct\)](#)  
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[www.cse.nd.edu/pub/Reports/1994/tr-94-40.ps.gz](http://www.cse.nd.edu/pub/Reports/1994/tr-94-40.ps.gz)

[A Floor Boundary Sensor for Autonomous Robot Navigation - Bellutta And \(Correct\)](#)  
vector ffl control and prediction module ffl system **model**. The image measurements are measurements, for  
the white of the floor and the black of the **baseboard**. In a different environment, a somewhat 4  
[alpha-bits.ai.mit.edu/people/bellutta/cv/fbs.e.ps.Z](http://alpha-bits.ai.mit.edu/people/bellutta/cv/fbs.e.ps.Z)

[Thermal simulation of the ATLAS SCT barrel module 27/5/97 - Thermal Simulation \(Correct\)](#)  
safety margin for the thermal runaway. 1. Module **models** Eight different module geometries as listed in  
RAL in mid March, 1997 **model-1** BeO picture-frame **baseboard model-2 model-1** but with a longer PG basebaord  
[atlas.kek.jp/~kondo/sct/INDET-NO-201.ps](http://atlas.kek.jp/~kondo/sct/INDET-NO-201.ps)

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












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